

Amendments to the Specification:

Please replace paragraph [0043] with the following amended paragraph:

[0043] As shown in FIGS. 2 and 3, respective pluralities of gate wires and storage wires of metal or conductive material, such as aluminum (Al) or aluminum alloy, molybdenum (Mo) or molybdenum-tungsten (MoW), chromium (Cr), and tantalum (Ta), are formed on an insulating substrate 10. Each [[A]] gate wire includes a gate line (or scanning signal line) 22 extending in the horizontal direction in FIG. 2 [[1]] and transmitting a scanning signal, and a gate electrode 24, which is a part of the gate line, and forms one terminal of a thin film transistor. The gate wire may include a gate pad connected to an end of the gate line 22 and transmitting a scanning signal from an external circuit to the gate line 22. Each [[A]] storage wire includes a storage electrode line 26 formed parallel to an adjacent one of the gate lines 22. Each storage electrode line 26 [[It]] is provided with a voltage, such as a common voltage applied to a common electrode (not shown) on an upper panel (not shown) of the liquid crystal display. Each of the [[The]] storage wires also includes a plurality of ladder-shaped storage electrodes 28 extending in the vertical direction in FIG. 2 [[1]], each of which comprises a pair of parallel, horizontally extending "rung"-like storage electrode connection portions 27 extending between a pair of parallel, spaced-apart, vertically extending "riser"-like elements respectively disposed on opposite sides of an associated one of the data lines 62, which enable an electrical connection to be made with a connecting the storage wire of a neighboring pixel row in a repair method described in more detail below, as well as [[and]] a flag-shaped wire repair portion 29 connected to an upper end of the ladder-shaped storage electrode 28. The storage wire[[s]] portions 26, 27, 28, and 29 of each a neighboring pixel row are all electrically interconnected, and connected at three places including the two storage electrode connection portions 27 and the storage electrode line 26, but optionally, each storage electrode 28 of a pixel row (except those in the first and last columns of the panel) may also be connected on either side to the storage electrode 28 of an adjacent pixel column by a horizontally extending redundant storage line 25, as illustrated in FIG. 4B and described in more detail below connection portions 27 and the storage electrode line 26. Each of the [[The]] storage wire[[s]] portions 26, 27, 28, and 29, along with an associated pixel electrode 82, which is described in more detail below, forms a storage capacitor (CST in FIG. 1) to provide storage capacitance along with a pixel electrode 82 which will be described later. Here, the

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common voltage that is applied to the storage wire portions 26, 27, 28, and 29 may be input either through one pad, and along one direction of the storage electrode line 26, or alternatively, through a plurality of pads, and along using both directions of the storage electrode line 26.